

An Evaluation of Different I/O Techniques for Checkpoint/Restart

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Outline:



- Motivation
- Fault Tolerance
- Asynchronous checkpointing
- Implementation and overhead estimation model
- Performance results
- Conclusion



- Nowadays, the increasing computational capacity is mainly due to extreme level of hardware parallelism.
- The reliability of hardware components does not increase with the similar rate.
- With future machines, the Mean time to failure is expected to be in minutes and hours.
- Absence of fault tolerant environment will put precious data at risk.



- **1.** Algorithm Based Fault Tolerance (ABFT)
- 2. Message Logging
- 3. Redundancy
- 4. Fault Prediction
- 5. Checkpoint/Restart (C/R)
 - State of each process is periodically stored to a stable storage
 - In case of a failure, application can be restarted from these states
 - Three types^{*}:
 - 1. Application level 2. User level 3. System level
 - Checkpoint overhead can be huge
 - Checkpoint frequency is a critical factor
 - Main bottleneck: I/O bandwidth

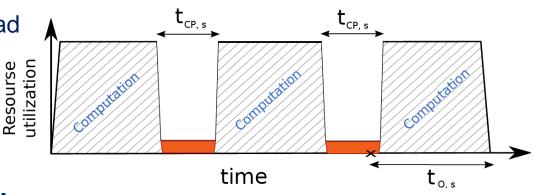
Each of these fault tolerance approaches carries overhead in terms of time and/or resources.

* J. Hursey, "Coordinated Checkpoint/Restart Process Fault Tolerance for MPI Applications on HPC Systems," Ph.D. dissertation, Indiana University, Bloomington, IN, USA, July 2010



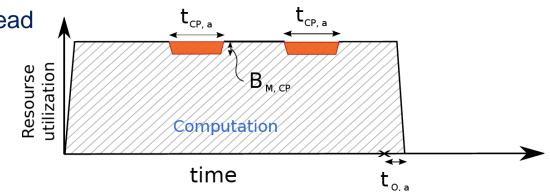
Synchronous checkpointing:

- Computation halts for I/O time.
- High execution time overhead



Asynchronous checkpointing:

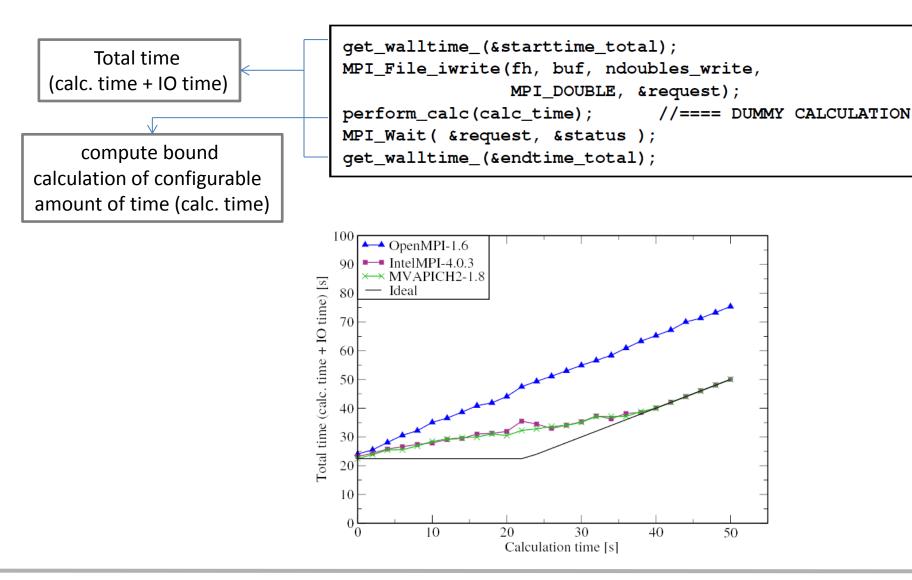
- Using dedicated threads for performing asynchronous I/O
- Low execution time overhead
- An in-memory copy of checkpoint is required.



In principle, non-blocking MPI-IO can be used to perform asynchronous checkpointing!

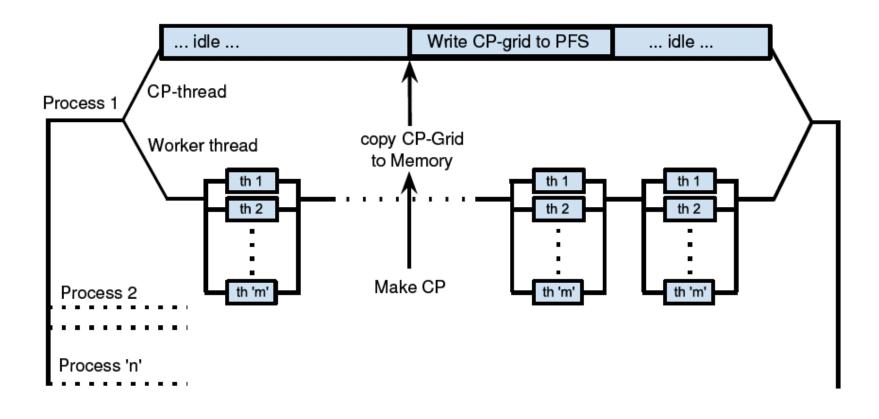
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Hybrid (MPI/OpenMP) parallel approach



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Execution options with hybrid approach on SMT enabled CPUs

<u>1 CP-thread per core</u>	<u>1 CP-thread per socket</u>	<u>1 CP-thread per node</u>
Socket 1 Socket 2 Socket 3 Socket 4 Socket 1 Socket 2 Socket 3 Socket 4 Socket 1 Socket 3 Socket 4 Socket 1 Socket 3 Socket 4 Socket 1 Socket 2 Socket 3 Socket 4 Socket 1 Socket 1 Socket 2 Socket 3 Socket 1 Socket 1 Socket 1 Socket 1 Socket 2 Socket 3 Socket 4 Socket 1 Socket 3 Socket 4 Socket 4 Socket 3 Socket 4 Socket 4 Socket 4 Socket 4 Socket 3 Socket 4 Socket 4 <td>Node Socket 1 Socket 2 Image: Im</td> <td>Socket 1 Socket 2 Socket 1 Socket 2 Socket 2 Socket 2 Socket 2 Socket 2 Socket 3 Socket 4 Socket 4 Socket 4 Socket 5 Socket 4 Socket 6 Socket 4 Socket 7 Socket 1 Socket 1 Socket 2 Socket 1 Socket 1 Socket 1</td>	Node Socket 1 Socket 2 Image: Im	Socket 1 Socket 2 Socket 1 Socket 2 Socket 2 Socket 2 Socket 2 Socket 2 Socket 3 Socket 4 Socket 4 Socket 4 Socket 5 Socket 4 Socket 6 Socket 4 Socket 7 Socket 1 Socket 1 Socket 2 Socket 1 Socket 1 Socket 1
	:	 process/thread Idle SMT core Checkpoint-thread



Application:

• A prototype CFD solver based on Lattice Boltzmann Method (LBM).

Cluster:

- LiMa (Erlangen) : QDR Infiniband cluster, 500 nodes (Dual socket Intel Xeon 5650 "Westmere"), Lustre based PFS Bandwidth ~ 3GB/s
- HERMIT (Stuttgart): CRAY XE6, 3552 nodes (Dual socket AMD Opteron 6278 "Interlagos"), Lustre PFS ~ 150 GB/s

Approaches:

- Synchronous CP
- Asynchronous CP
- Scalable Checkpoint Restart (SCR) Library

Implementation with LBM



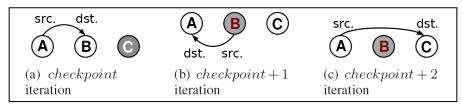
• Worker-thread:

- Performs computation iterations
- Creates in-memory copy of the checkpoint and signals the CP-thread.

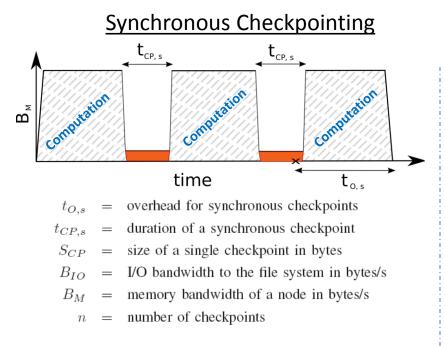
Checkpoint-thread:

- Waits for the signal from worker-thread.
- Writes the checkpoint PFS.

 For "toggle grids" based stancil algorithm (e.g LBM), effective pointer switching can be used to avoid inmemory copy of the checkpoint.

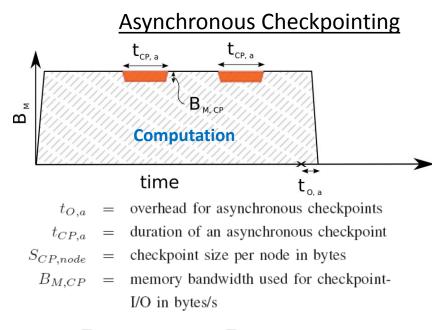






$$t_{O,s} = n \cdot t_{CP,s}$$
$$t_{O,s} = n \cdot \frac{S_{CP}}{B_{IO}}$$

For weak scaling, overhead is directly proportional to the number of nodes



$$B_M \cdot t_{O,a} = n \cdot B_{M,CP} \cdot t_{CP,a}$$

For I/O purposes, the amount of data traffic (reads/writes) between memory and processor can be "m" times larger than the file size itself. Our study reveals this factor to be between 5-7 for OpemMPI (m=5-7).

$$B_{M,CP} = \frac{m \cdot S_{CP,node}}{t_{CP,a}}$$
$$t_{O,a} = \frac{m \cdot S_{CP,node}}{B_M} \cdot n$$

Overhead remains constant for weak scaling.

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Validation of asynchronous overhead estimation model is done by using *likwid-perfctr^{*}* tool.

- Single socket LiMa cluster Memory bandwidth of each 30000 no-CP SO socket is measured every Bandwidth[MBytes/s] Async-CP S0 500ms. Svnc-CP S0 25000 20000 **Estimated overhead:** 15000 ■ 2.2s (n=2, S_{cp,node}=6.25GB, Socket Mem. 10000 synchronous $B_M = 40 GB/s, m = 7$) checkpoint is being written 5000 Actual overhead: 25 100 50 75 2.6s Application Runtime[s] No checkpoint \leftarrow Async. checkpoint \leq
- * https://code.google.com/p/likwid/

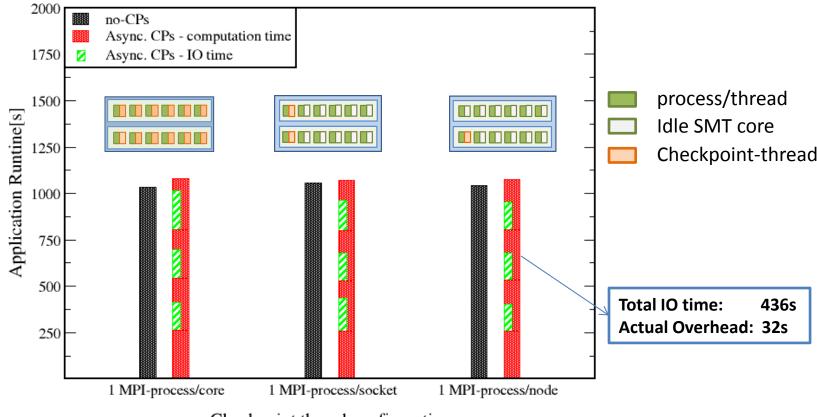
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Sync. checkpoint



Hybrid (MPI-OpenMP) configuration performance comparison

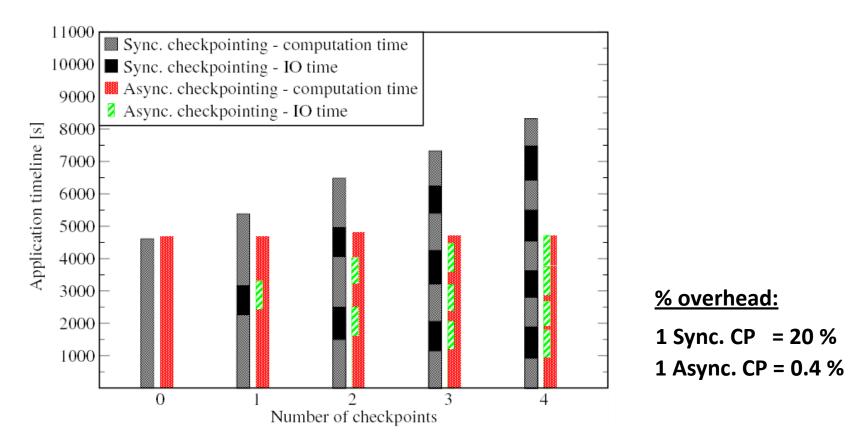
Cluster: LiMa, num. of nodes = 32, PFS = LXFS, Aggregated CP size = 200 GB/CP



Checkpoint thread configuration

LiMa

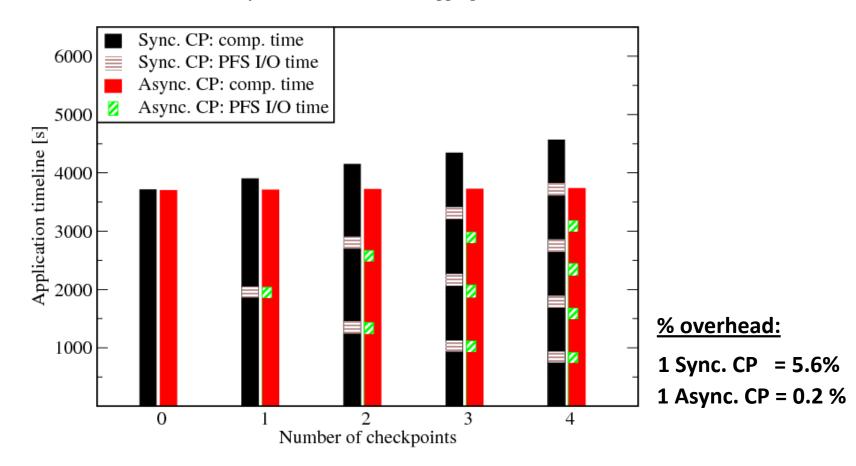
Num. of nodes = 128, np = 1536, PFS = LXFS, Aggregated CP size = 800GB/CP



FFEE

HERMIT

Num. of nodes = 256, np = 8192, PFS = Lustre, Aggregated CP size = 2.3TB/CP

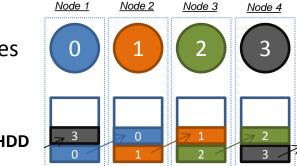


Scalable Checkpoint Restart

- Scalable Checkpoint/Restart is a library developed by LLNL(Adam Moody)^{*}
- Key idea: Node-level checkpoints (memory, Hard disk)
- Checkpointing Features
 - LOCAL
 - PARTNER

MPI Processes

Local node memory/SSD/HDD



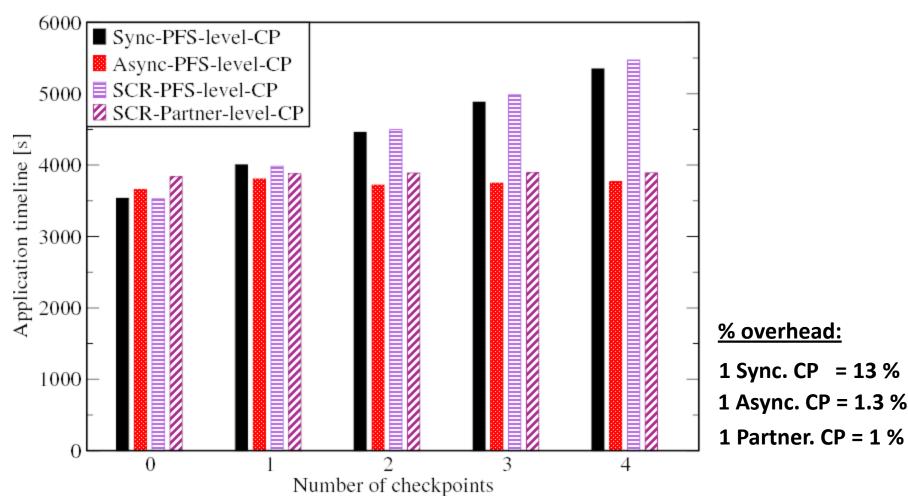
- PARTNER XOR
- Parallel File System (PFS) level checkpoints
 - To deal with catastrophic failures

* http://sourceforge.net/projects/scalablecr/

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LiMa



Num. of nodes = 128, PFS = LXFS, Aggregated CP size = 510 GB /CP

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- Effective implementation of C/R and effective resource utilization can reduce overhead to minimum level.
- The overhead due to I/O bottlenecks can be reduced with asynchronous checkpointing approach.
- Although SCR on node-level is highly scalable, PFS-level checkpoints carry less overhead with asynchronous approach.



Thank you! Questions?

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