

Exploiting Performance Benefits of Extruded Meshes in PyOP2

Department of Computing - Software Performance Optimisation Group Imperial College London

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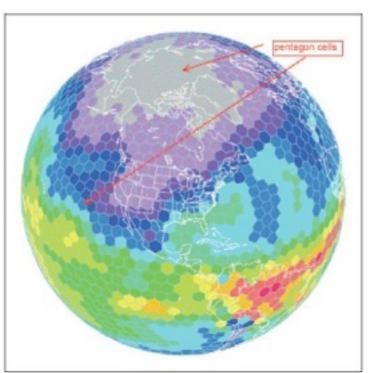
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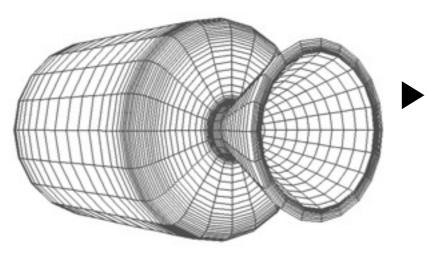
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Mesh-Based Simulation Applications







Atmosphere and ocean modelling

- Climate models and numerical weather prediction
 - Thin-shell object simulations

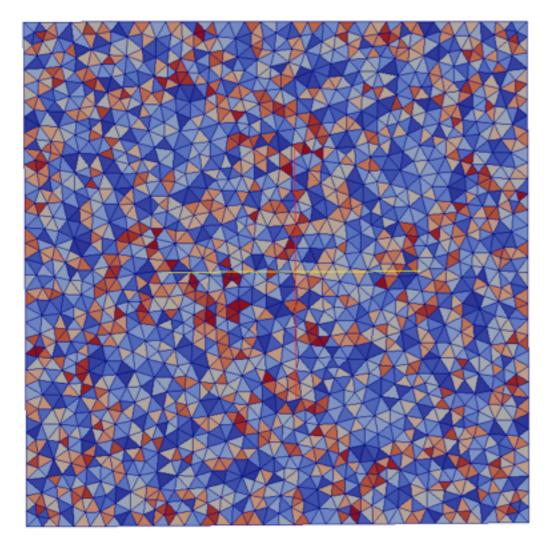
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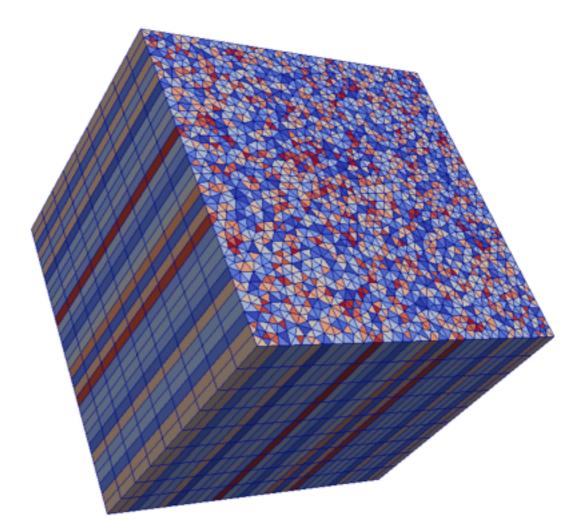
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Types of Meshes

- Unstructured & structured meshes
- ▶ Hybrid: unstructured in the 2D + structured in the 3rd dimension = <u>Extruded Meshes</u>.





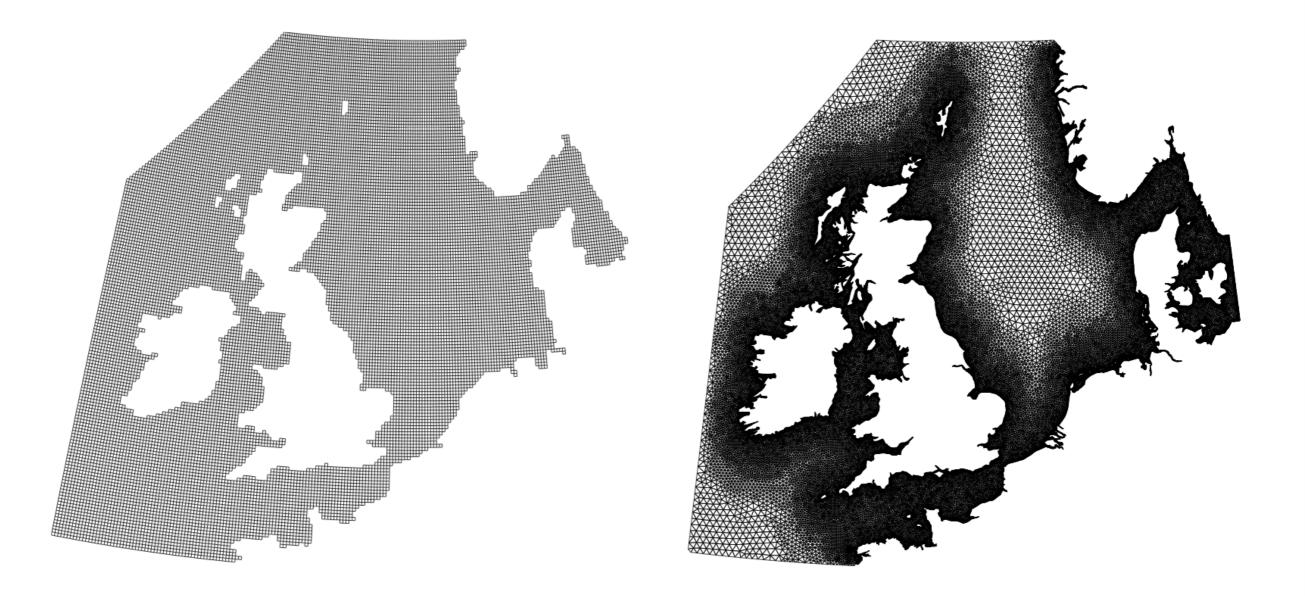
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Advantages of Extruded Meshes of 2D unstructured base-meshes <u>Flexibility, Accuracy.</u>



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What do all these applications have in common?

The type of operations:

<u>The application of the SAME</u> <u>computational kernel to EVERY member</u> <u>of a discrete set of mesh elements.</u>

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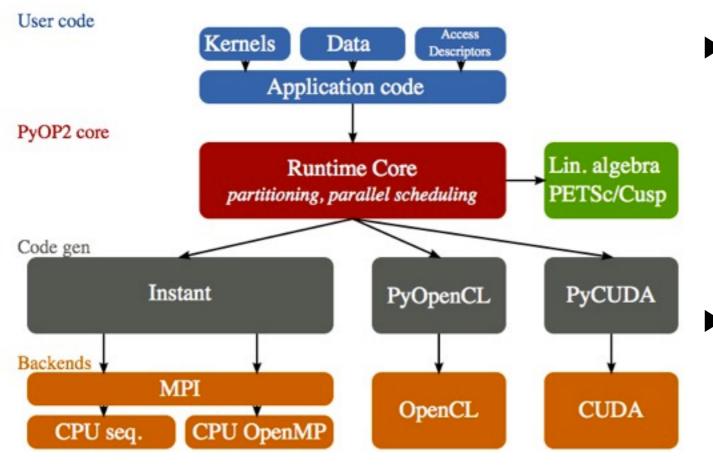


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PyOP2

A Python implementation of the OP2 paradigm (Oxford Parallel Language for Unstructured Mesh Computations).



- Provides a high level <u>Domain Specific Language</u> (DSL) which translates code to a low level implementation through <u>runtime code generation</u>.
- Adds a <u>new layer of</u> <u>abstraction</u> for a <u>flexible</u>, <u>portable</u> and <u>scalable</u> implementation.

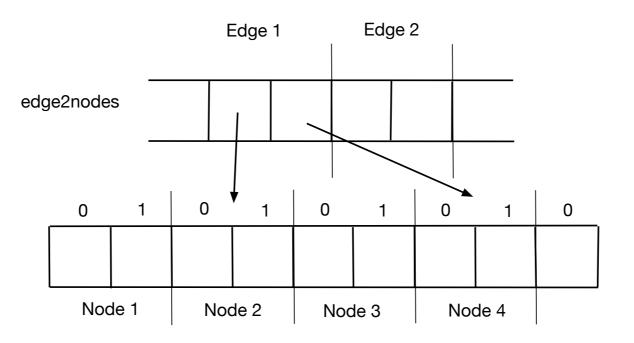
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The PyOP2 DSL

- ► <u>SETS</u> for mesh elements;
- Data arrays (DATs) for fields, coordinates;
- ▶ <u>MAPs</u> for the connectivity of mesh elements;
- ▶ <u>PARALLEL LOOPS</u> for performing the actual work.



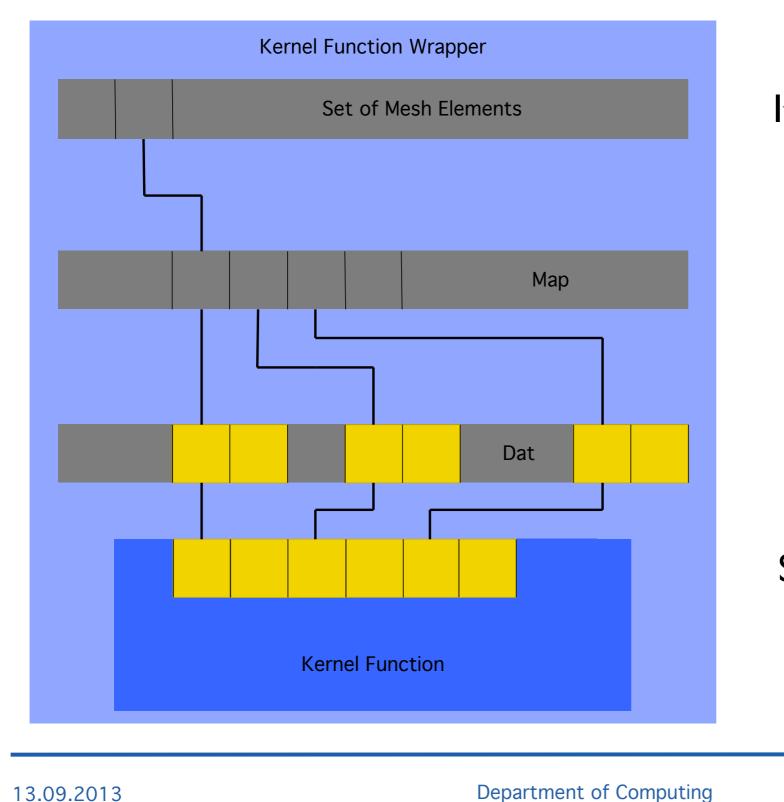


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Iterate over mesh elements

For each element use the map to reference data.

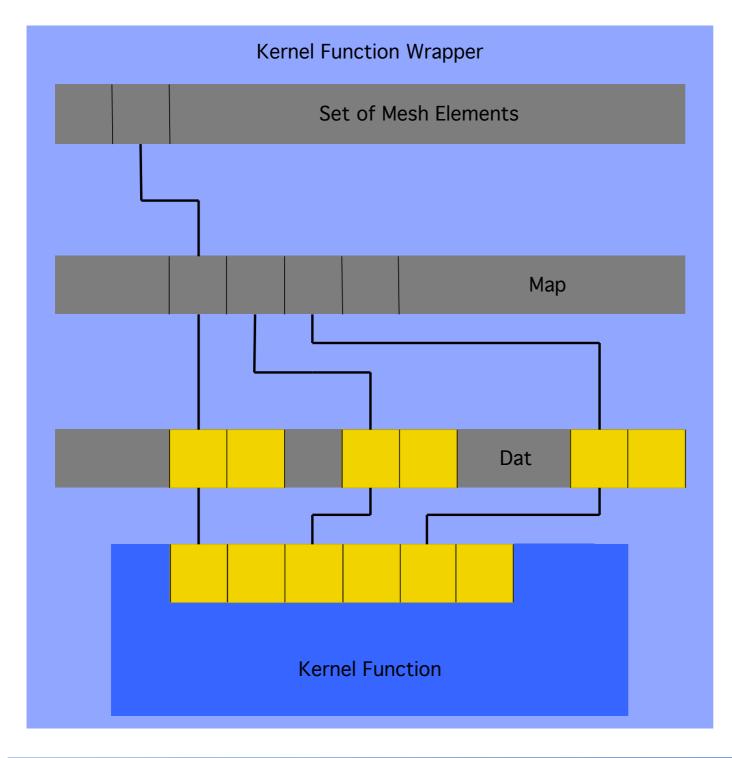
Stage-in data to be used by the kernel.

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Iterate over mesh elements

For each element use the map to reference data.

For each set of indirect element references iterate over the column elements.

Stage-in data to be used by the kernel.

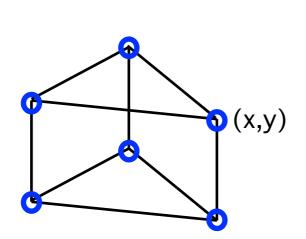
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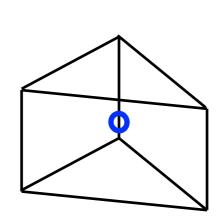
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A Minimal Test Problem

Tdx





Coordinate Field: Location of Degrees of Freedom Tracer: Location of Degrees of Freedom

Effectively we are aiming to perform a very simple experiment: <u>a global reduction</u> <u>operation</u>.

<u>No favours</u>: The mesh we will be using is big enough to ensure that no cache benefits will be observed between time steps.

- The 2D unstructured mesh contains: 806,000 cells.
- There are 100 time steps executed in total.

Data movement dominates computation!

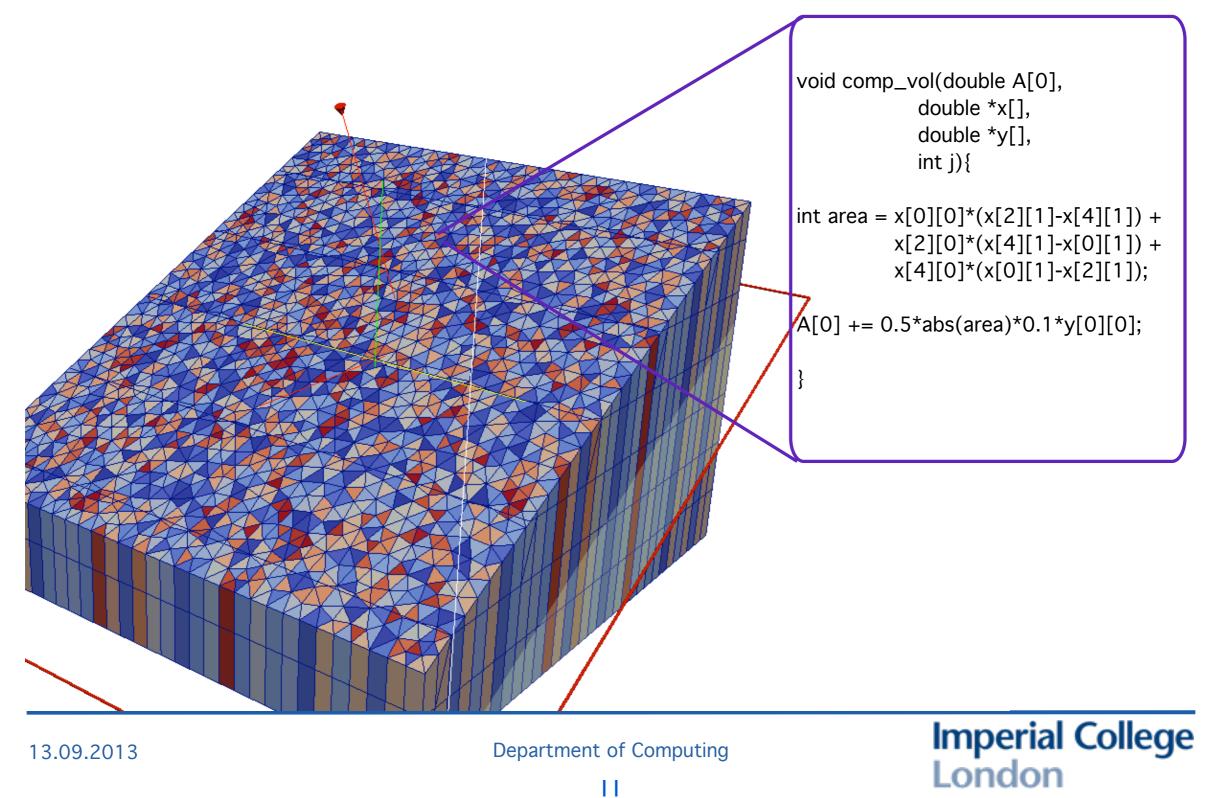
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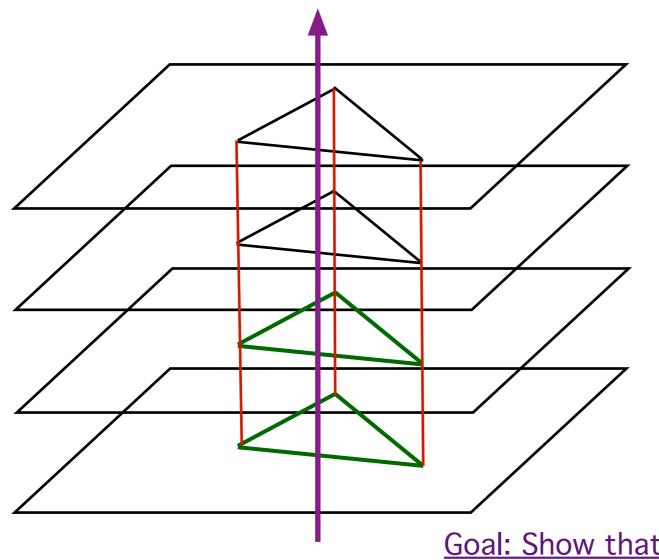


Kernel Application on extruded meshes





Using Extruded Meshes Efficiently



- We start from a 2D unstructured mesh.
- The 3rd dimension is structured.
- The innermost iteration occurs over the cells in the column.
- For each field we have just one indirection per column. Hence the penalty for the unstructured horizontal mesh is only paid once per column.

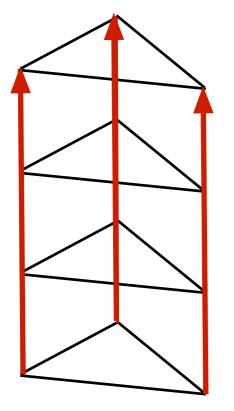
<u>Goal: Show that the accesses in the structured direction</u> <u>remove the performance penalty of the unstructured</u> <u>direction.</u>

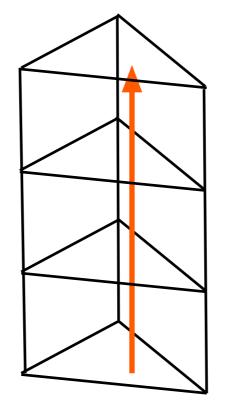
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Column Numbering - Vertical Data Locality





Vertical numbering of the mesh :

- Each group of degrees of freedom in the 2D will be "extruded" vertically for each of the layers.
- Numbering will be continuous as we want all the elements of the column to occupy a contiguous area in memory.

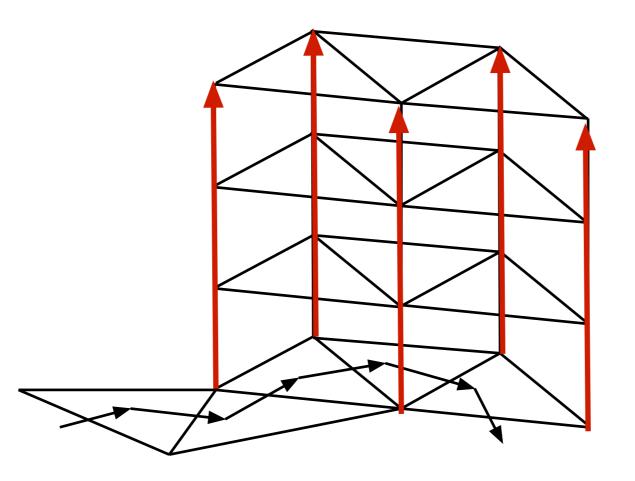


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Mesh Numbering - Data Locality in the 2D

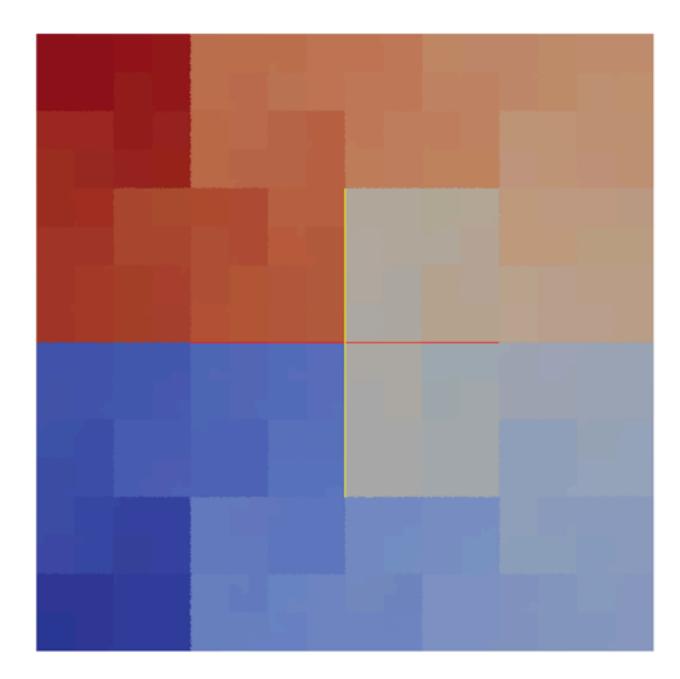
<u>Using a space filling curve to renumber the 2D mesh</u> will ensure temporal locality of the indirections.







This is how a good numbering looks:

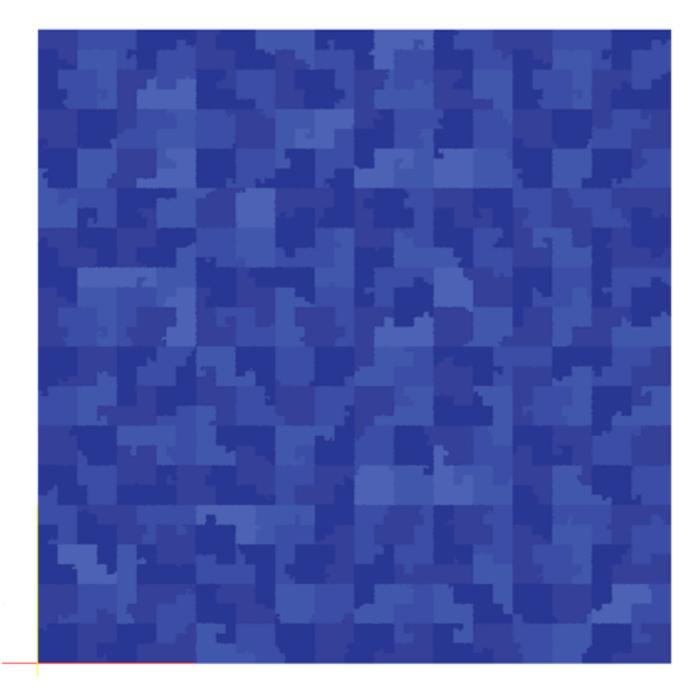




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Partitioning and Colouring



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The hardware

CPU type: ********	Intel Core San	dyBridge processor ***********************************
Hardware Thr	ad Topology	******
Sockets:	1	
Cores per soc	et: 4	
Threads per c	ore: 2	
Socket 0: (0 4	152637)	

Level: 1 Size: 32 kB Cache groups: (04)(15)(26)(37)

Level: 2 Size: 256 kB Cache groups: (04)(15)(26)(37)

Level: 3 Size: 8 MB Cache groups: (04152637)

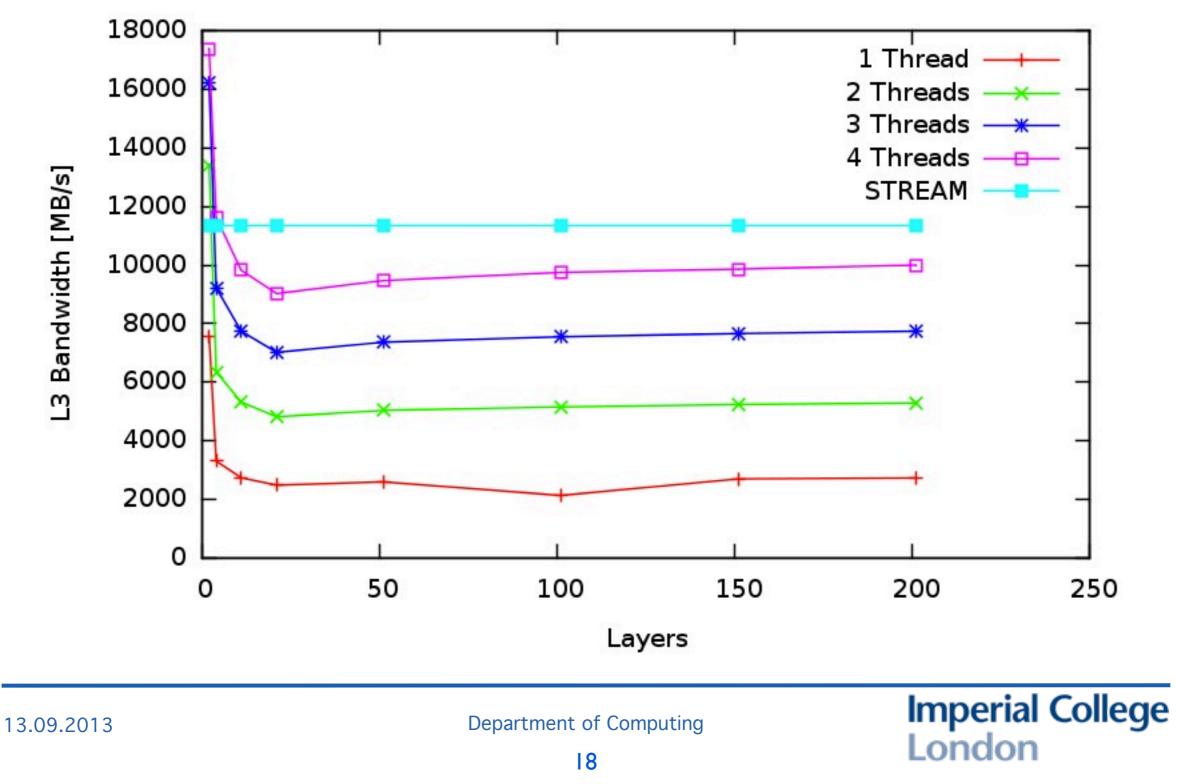
- Intel 4-Core (SandyBridge) i7-2600 CPU @ 3.40GHz
- Memory topology diagram using Likwid.

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L3 Cache Bandwidth STREAM Comparison using Likwid





Valuable Bandwidth

DV = Data Volume;

 $DV_{Coordinates} = Number of nodes \times Dimension \times Bytes per coordinate$

 $DV_{Tracer} = Number of cells \times Bytes per tracer value$

 $DV_{Total} = Outer Iterations \times Layers \times (DV_{Coordinates} + DV_{Tracer})$

 $Valuable Bandwidth = \frac{DV_{Total}}{Execution time}$

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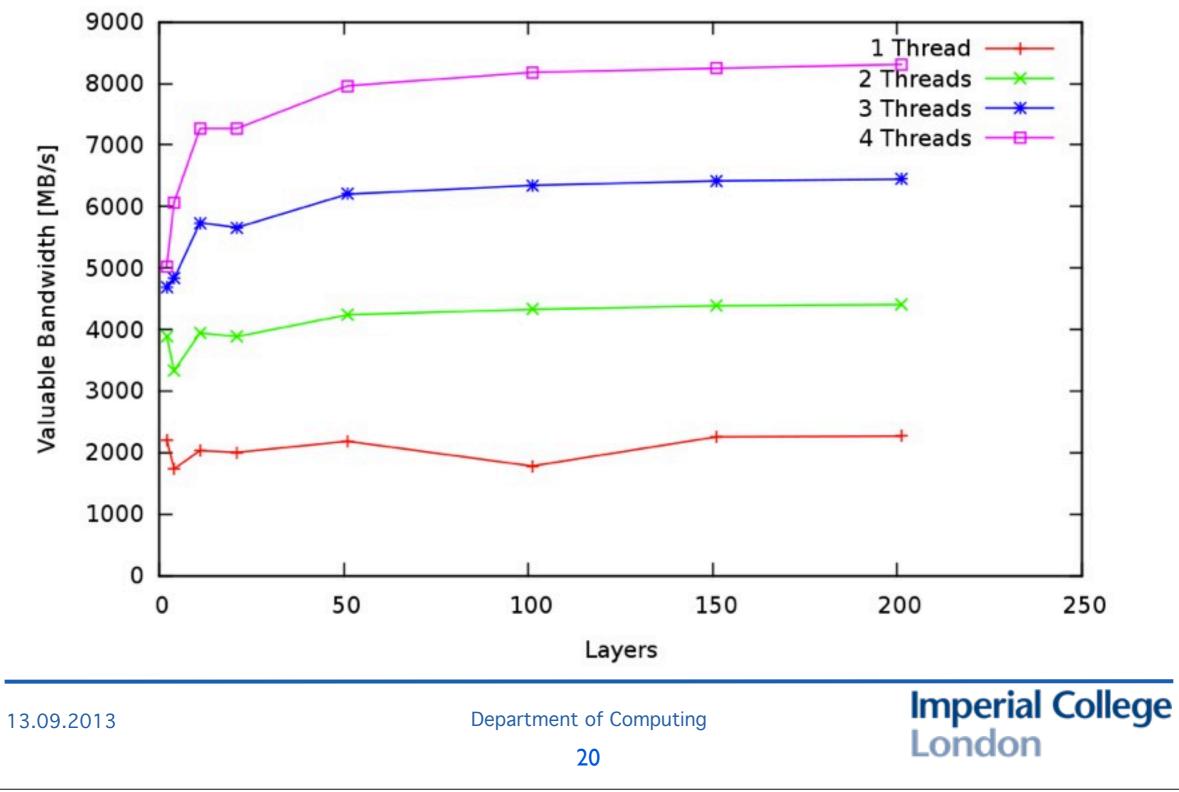
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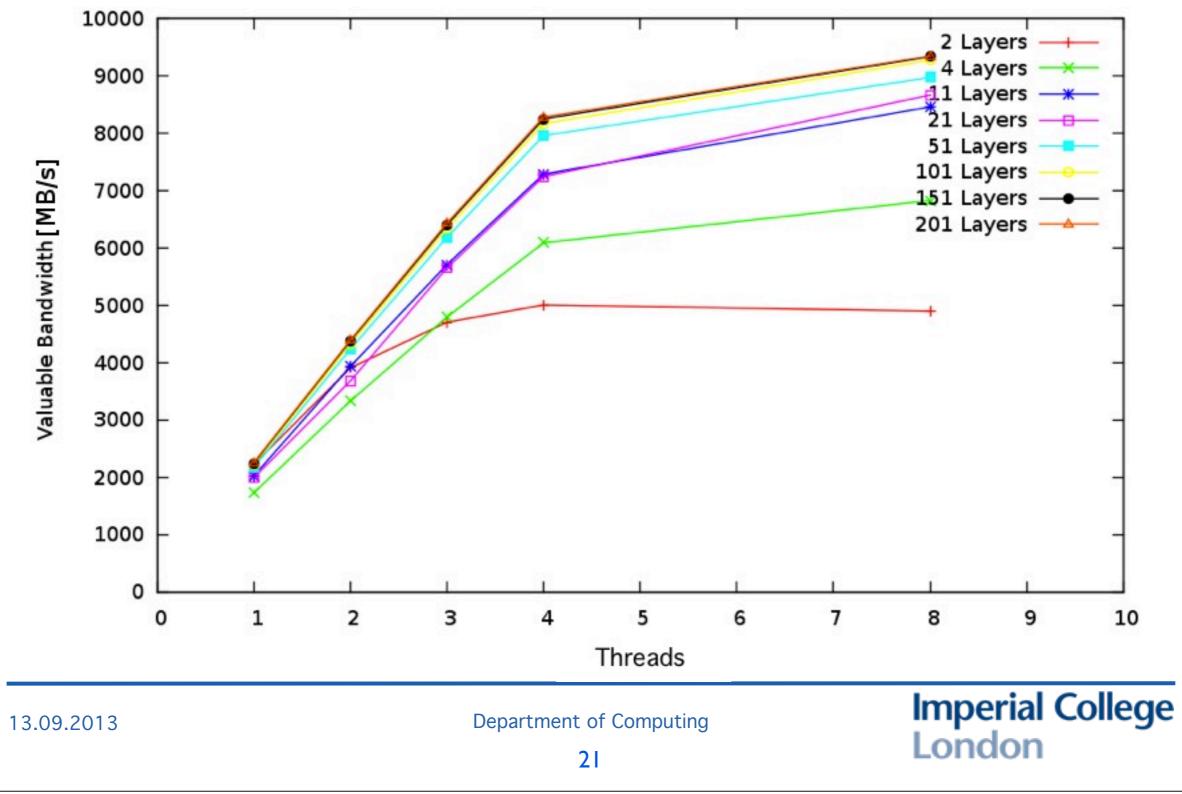


Valuable Bandwidth - a Lower Bound



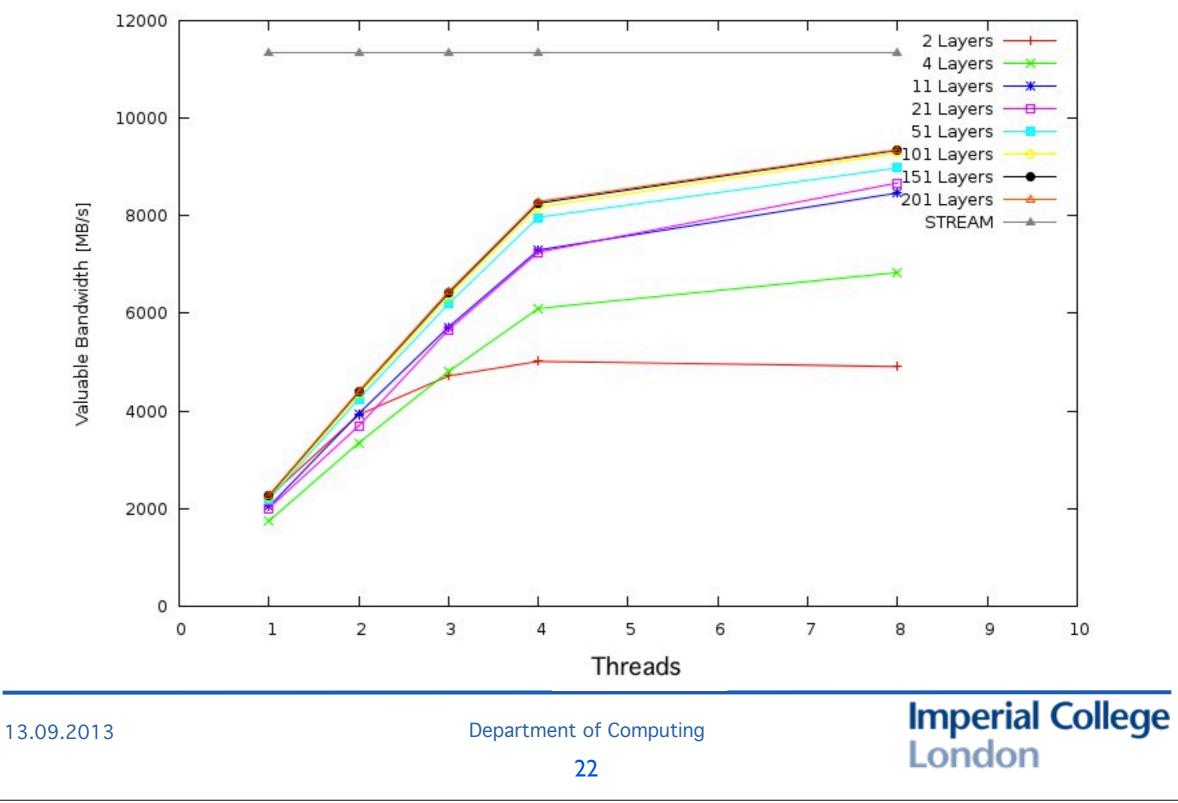


Valuable Bandwidth - Increasing thread count





Valuable Bandwidth - STREAM Comparison





Conclusions for this experiment

We consider the Valuable Bandwidth achieved with 8 threads and more than 100 layers and compare it with the STREAM bandwidth.

The <u>Valuable Bandwidth</u> achievement of this <u>bandwidth stress test</u> is <u>82.4%</u> of the STREAM benchmark bandwidth.

The <u>number of layers</u> needed to offset the penalty of using an unstructured mesh is about <u>20</u>.

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Remarks

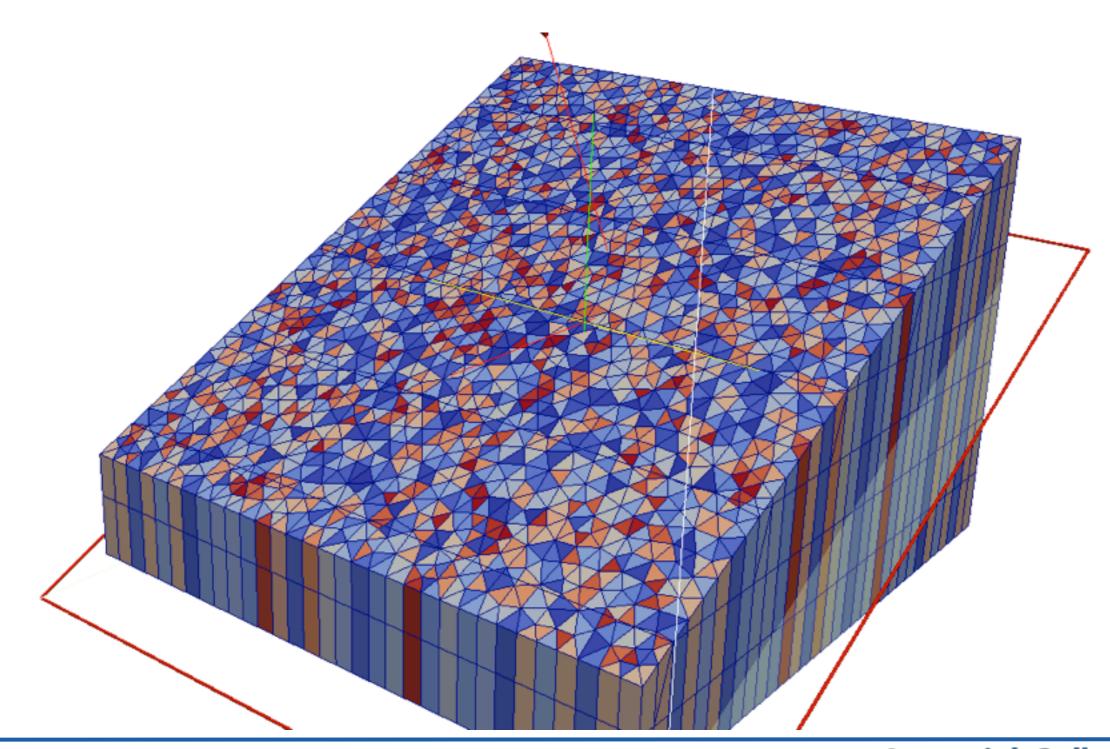
- We now know what makes a good Extruded Mesh.
- Location, location, location!
- Comparison with <u>STREAM</u> rather than a Structured Mesh code.
- Different slices through the memory hierarchy performed with Likwid show <u>similar performance</u> numbers to the STREAM benchmark.
- Limitations: only reading, only one platform, only single socket.

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Thank you!



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Solving Partial Differential Equations

```
# Define mesh, function space
mesh = Mesh("box_with_dent.xml.gz")
V = FunctionSpace(mesh, "CG", 1)
```

```
# Define basis and bilinear form
u = TrialFunction(V)
v = TestFunction(V)
a = dot(grad(u), grad(v))*dx
```

```
# Assemble stiffness form
A = PETScMatrix()
assemble(a, tensor=A)
```

```
# Create eigensolver
eigensolver = SLEPcEigenSolver(A)
```

```
# Compute all eigenvalues of A x = \lambda x
print "Computing eigenvalues. This can take a minute."
eigensolver.solve()
```

- Means starting from a high level specification of the problem and ending up with a low-level optimised implementation.
- The FEniCS Dolfin tool chain already does something similar:
 - Uses the <u>Unified Form</u> <u>Language (UFL)</u> to specify the problem.
 - Uses the <u>FEniCS Form Compiler</u> (FFC) to automatically generate the kernel code.
 - Uses the <u>Dolfin</u> backend <u>to</u> provide the code required to run the kernel function.

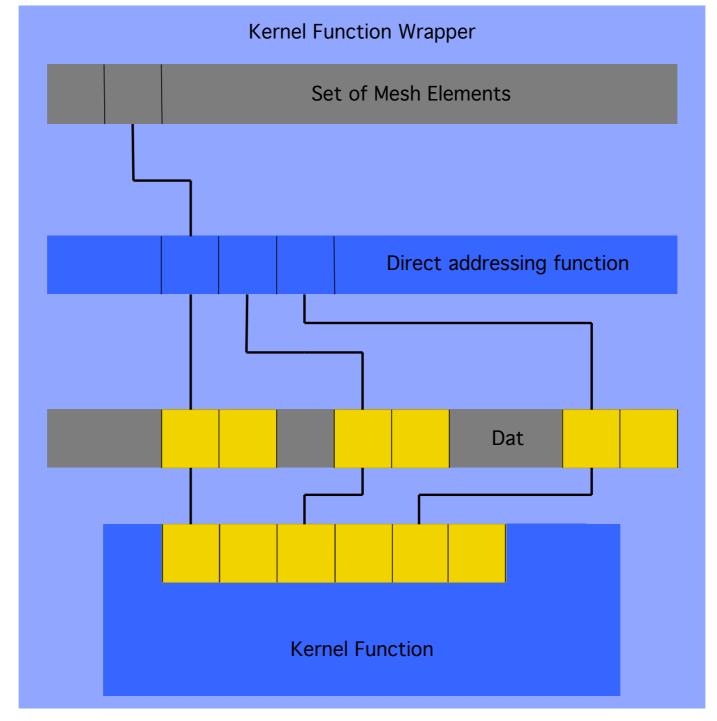


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A PyOP2 parallel loop - direct







Considerations for Exploiting the Structure of Data

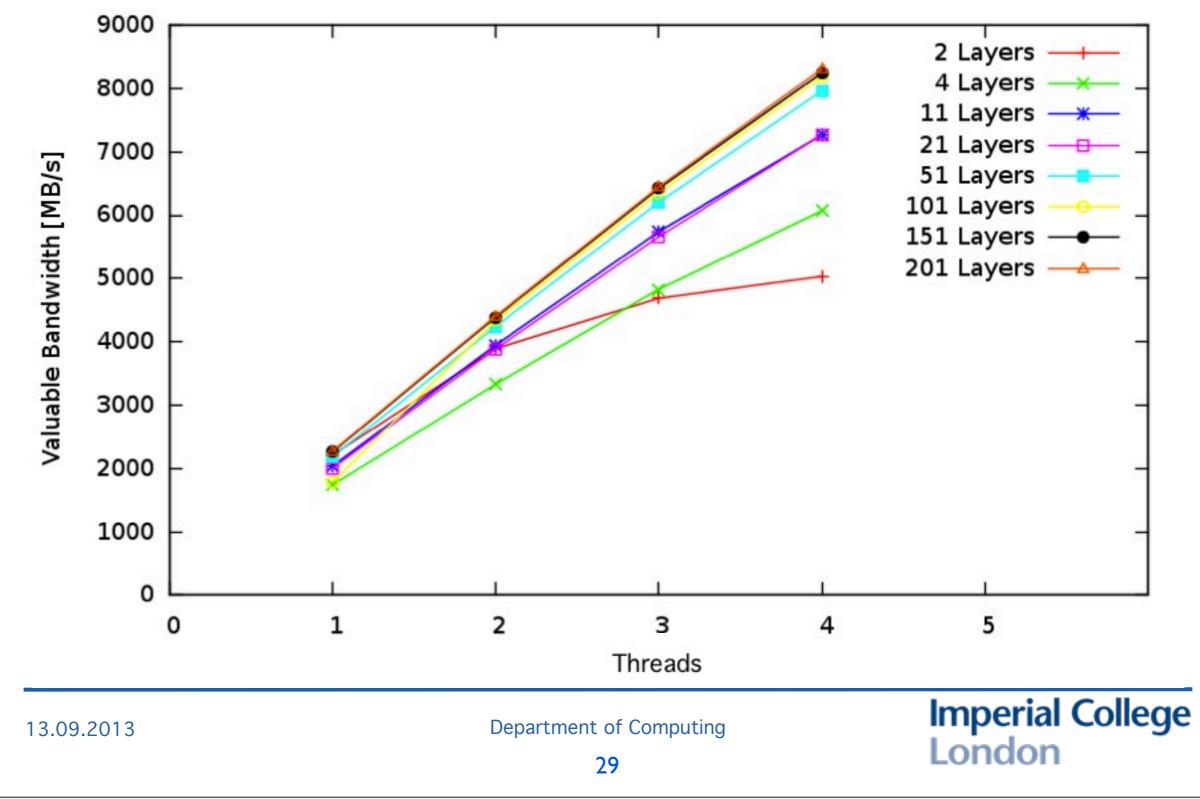
- There is a tight coupling between the structure of the mesh and the structure of the data.
- Performance is affected as the problem structure has a direct impact on data movement.
- Moving data efficiently leads to improved <u>scalability</u> saturating the bandwidth is not a question of "if" but a question of "when".
- Exploiting structure requires detailed knowledge of the particularities of each system architecture different micro-optimisations are required for different architectures so this affects <u>portability</u>.
- Being able to seamlessly switch between implementations provides <u>flexibility</u>.

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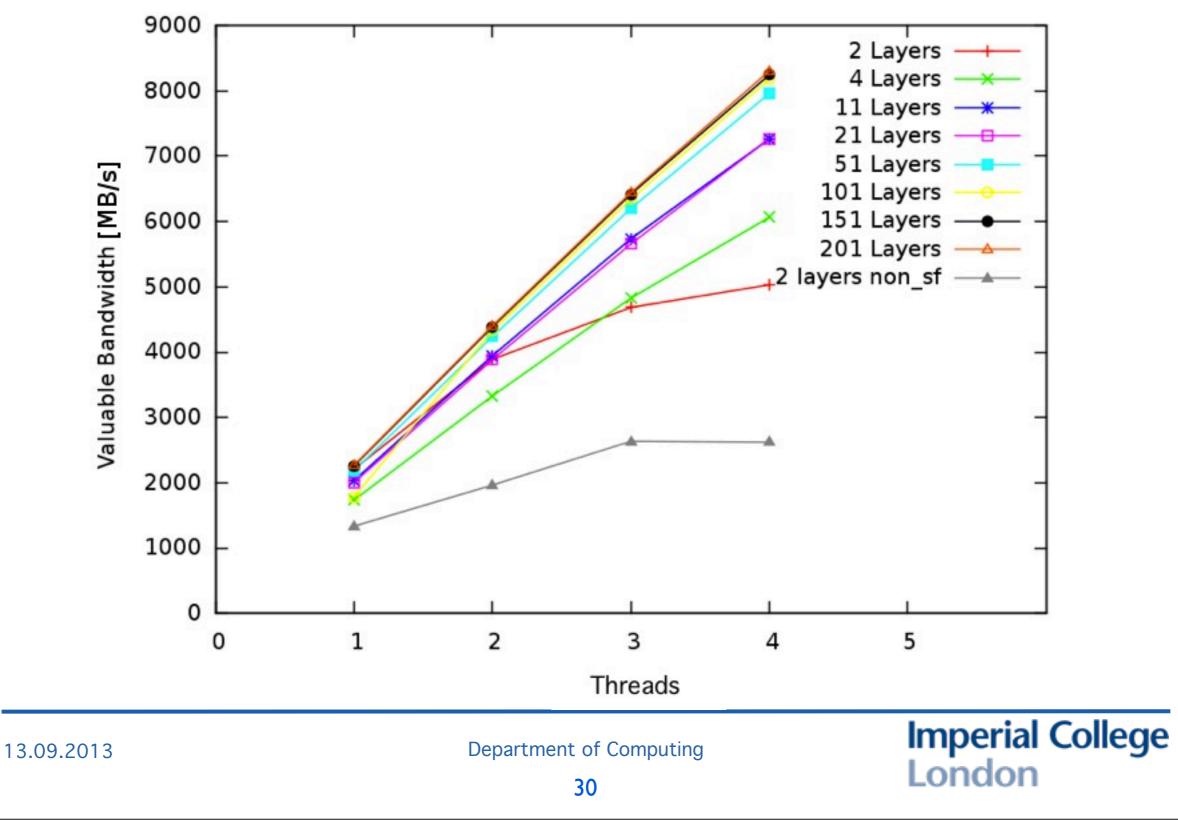


Valuable Bandwidth - a Lower Bound





Valuable Bandwidth - a Lower Bound





L2 Cache Bandwidth using Likwid 1 Thread 2 Threads 3 Threads 4 Threads L2 Bandwidth [MB/s] Layers Imperial College London Department of Computing 13.09.2013

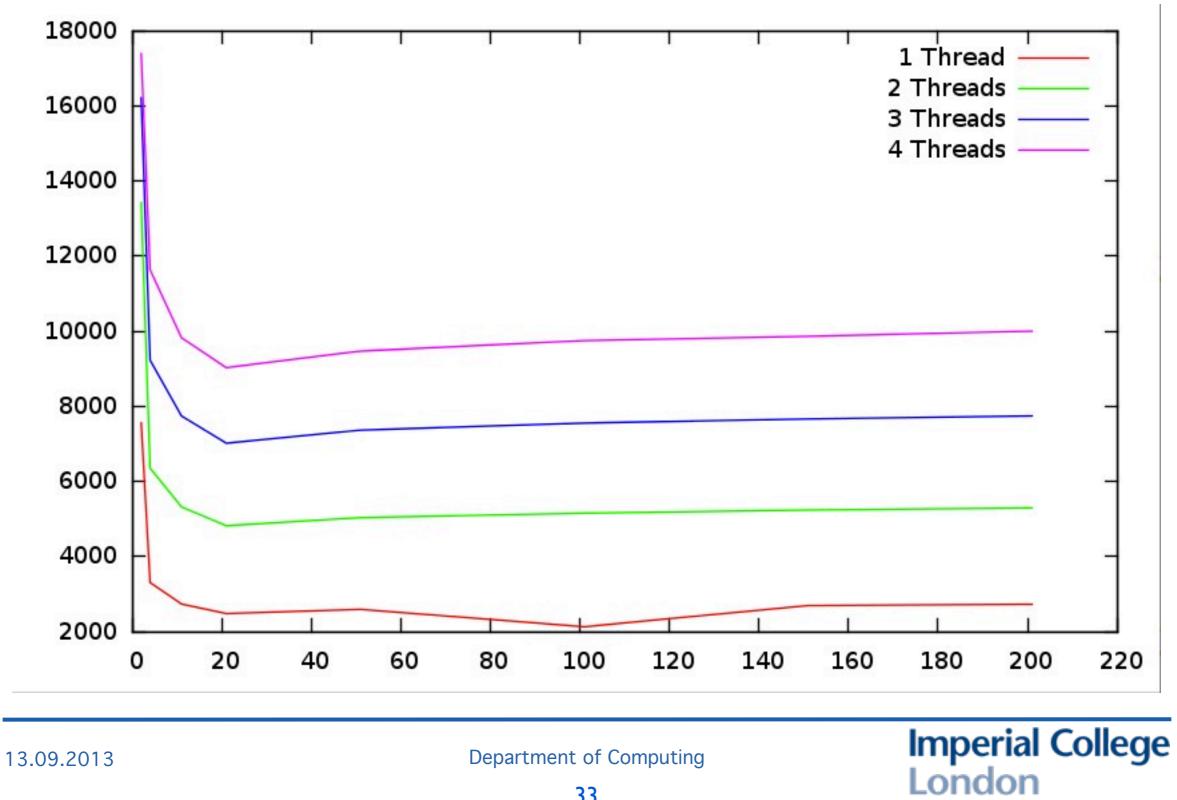


18000 2 Layers 4 Layers 17000 11 Layers 21 Layers 16000 51 Layers 101 Layers 15000 151 Layers 14000 201 Layers [MB/s] 13000 12000 11000 10000 9000 8000 500 1000 1500 2000 2500 3000 0 Imperial College London Department of Computing 13.09.2013 32

Partition Independence



L3 Bandwidth (Likwid) - Layers vs. Threads





Iterating over the Mesh

• for each colour C

- for each partition P in C
 - for each 2D cell in partition P
 - for each cell in the column

• apply Kernel

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